

THREE STATE LOGIC PROBE

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Abstract:

Created a logic level detection circuit of a digital circuit. This circuit has the ability to 3 (three) logic level digital signal is typically defined as follows: Level of high voltage (High), low voltage level (Low) and the threshold voltage level (Threshold).

The ability of this series allows a digital circuit analysis to help level the better. In this series of tests in particular applied to the gates of the base.

Bibliography: 4, (1981 – 1996)

